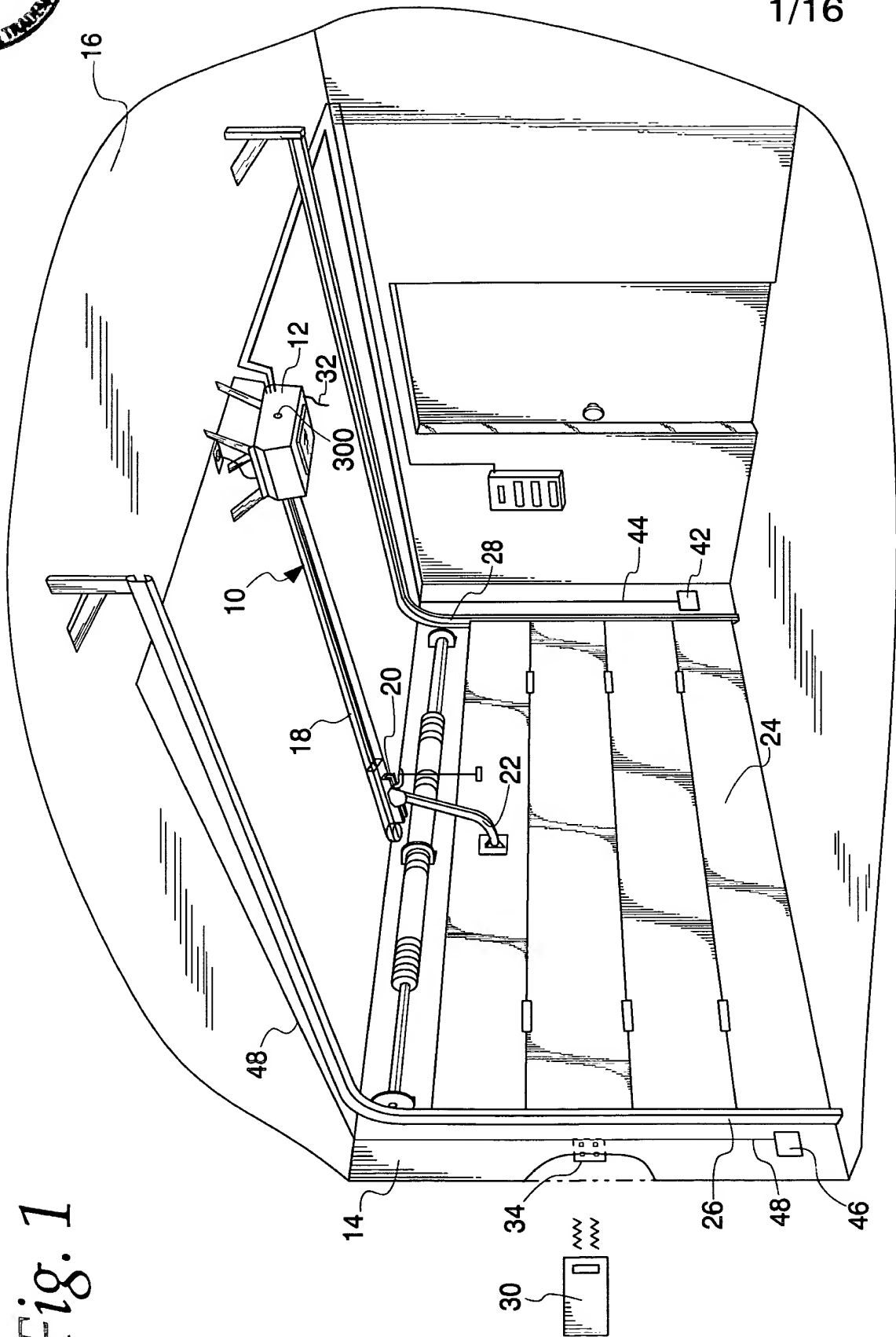




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Fig. 1



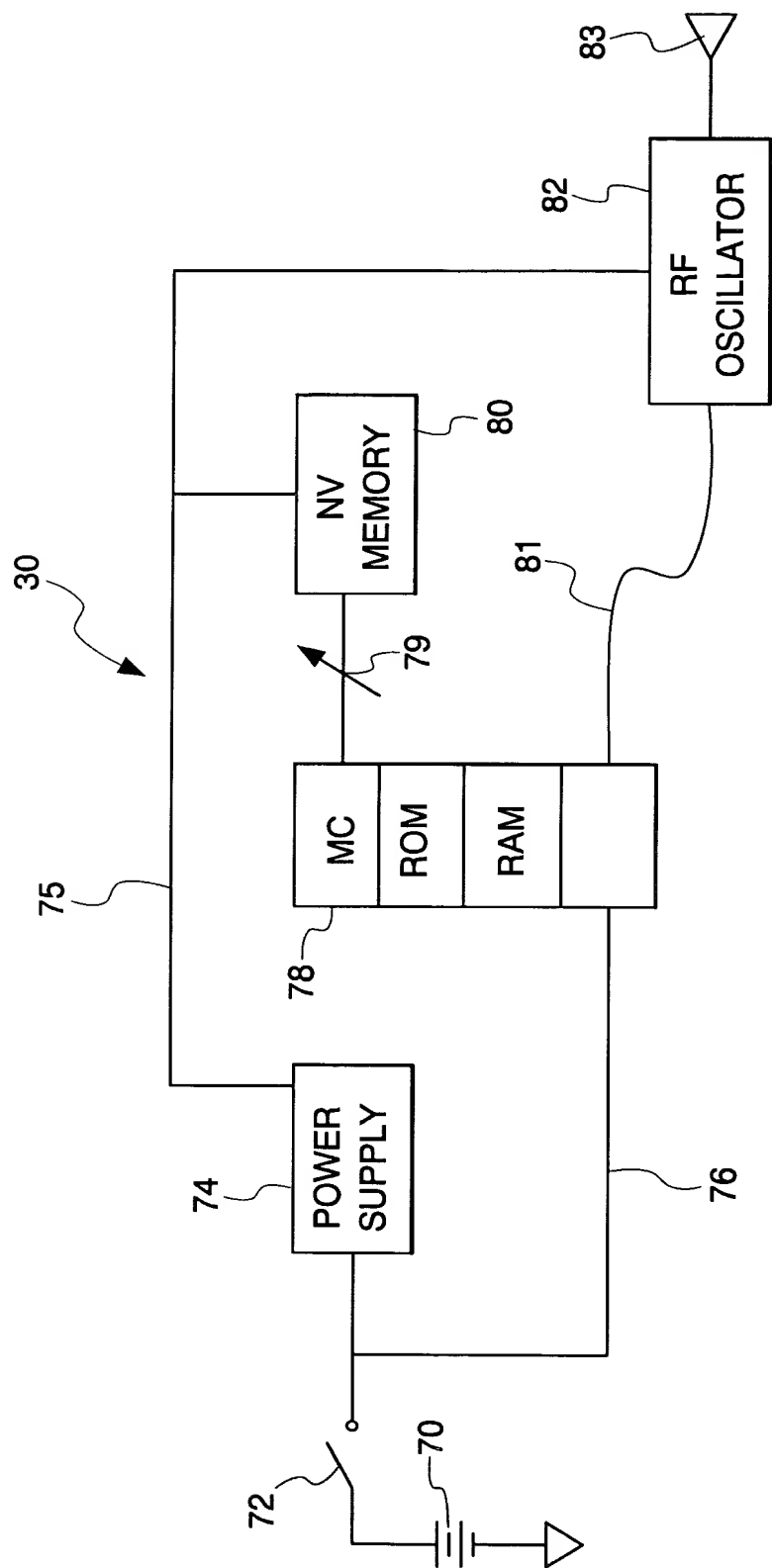


Fig. 2

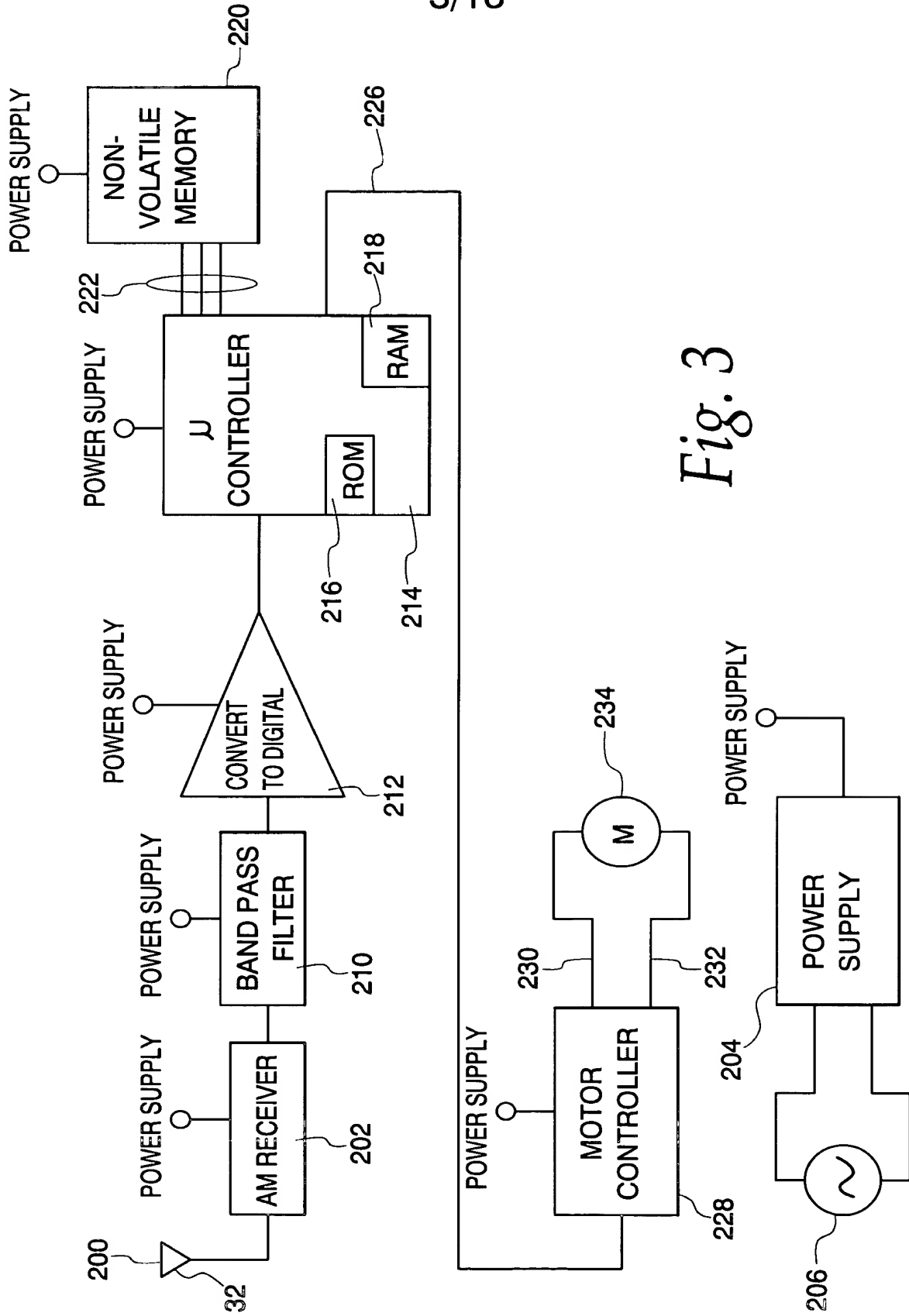


Fig. 3

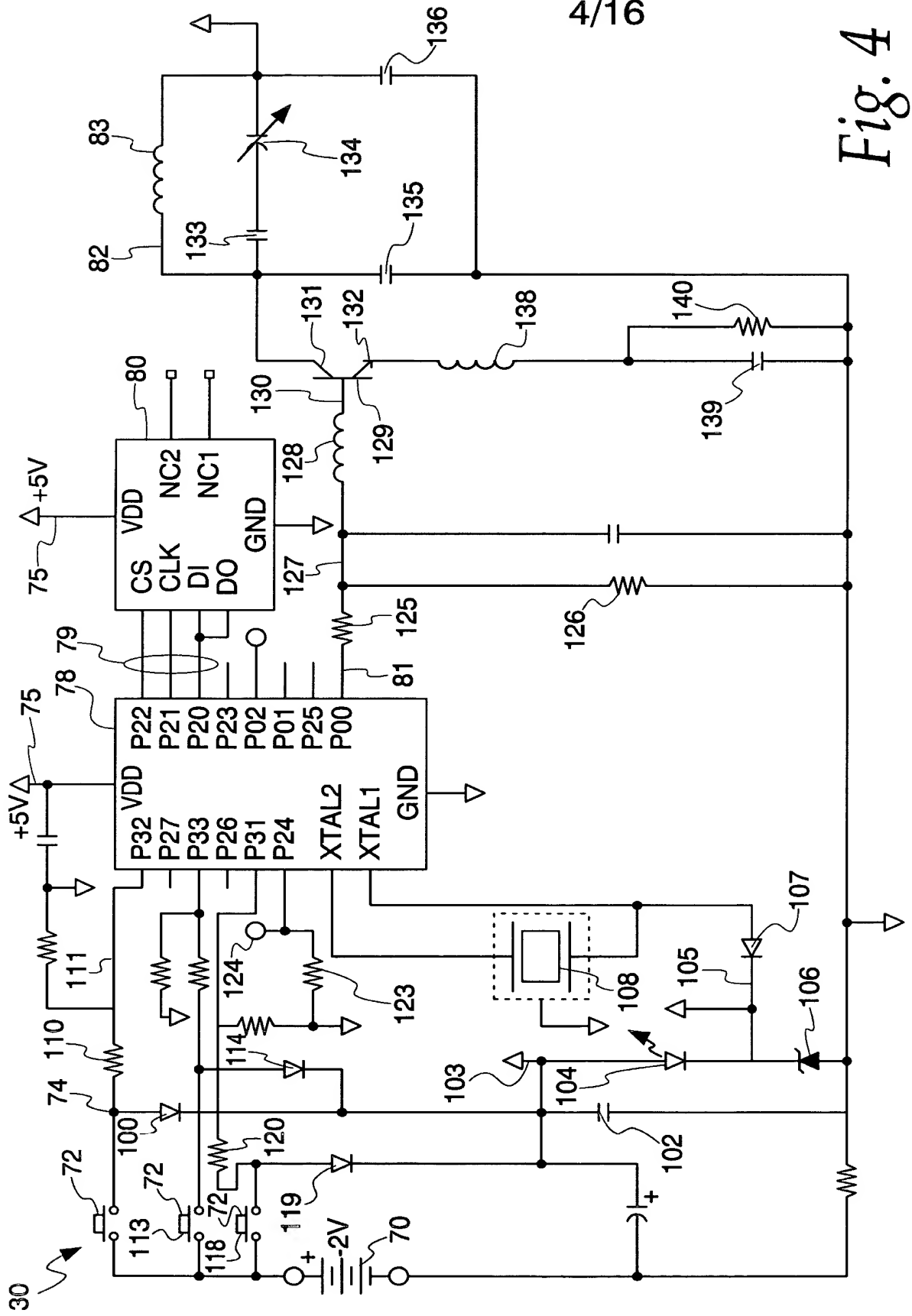
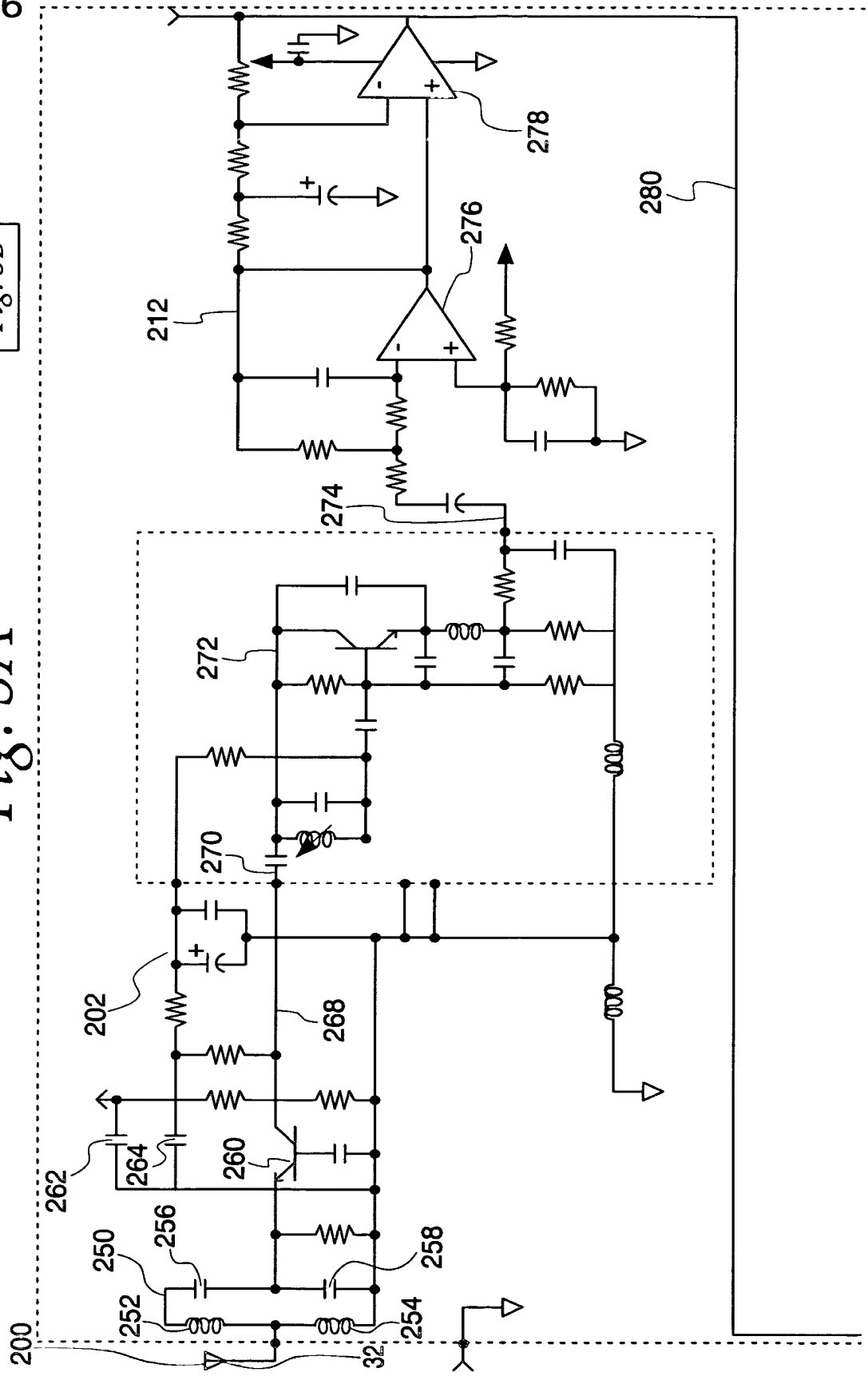
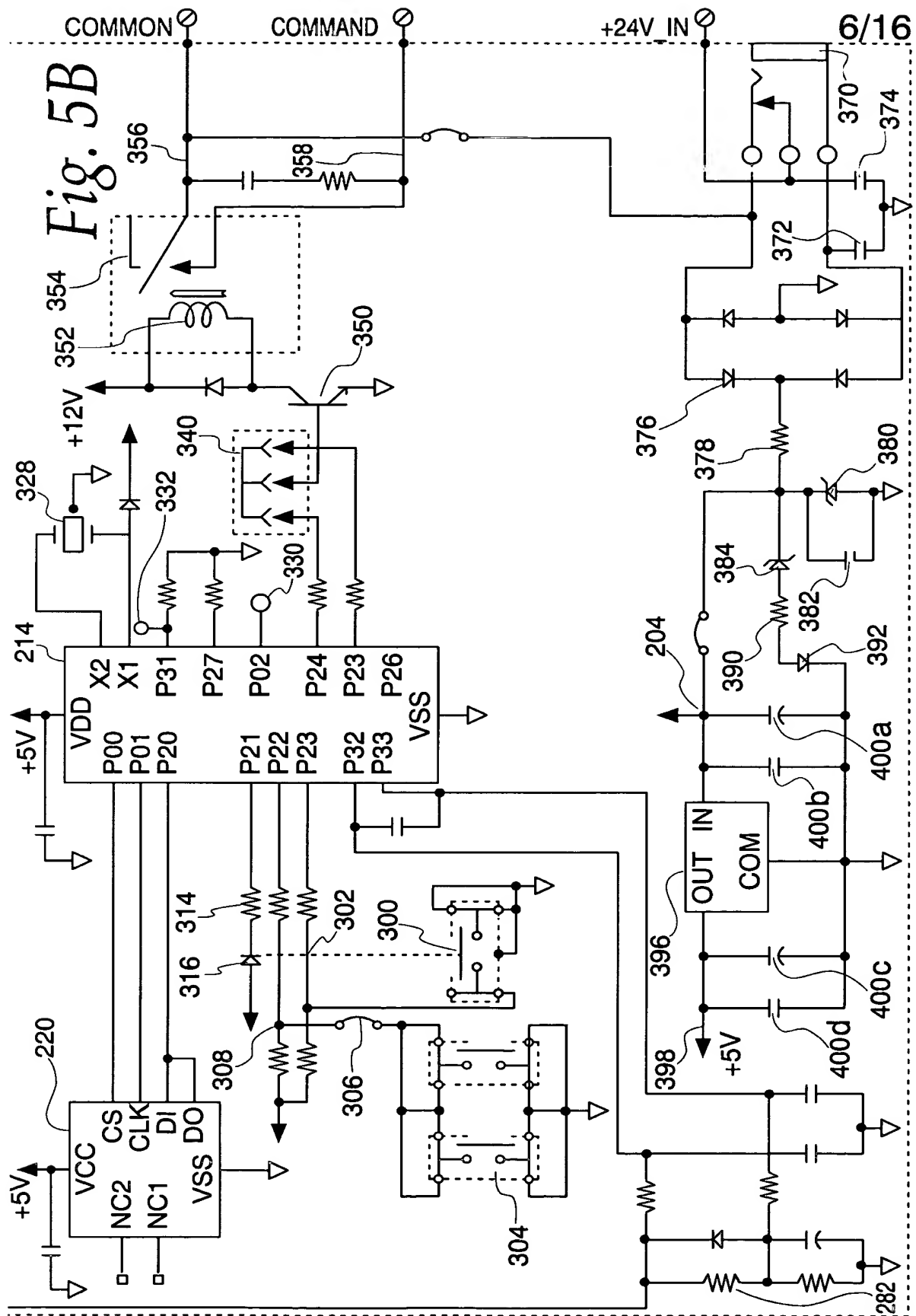


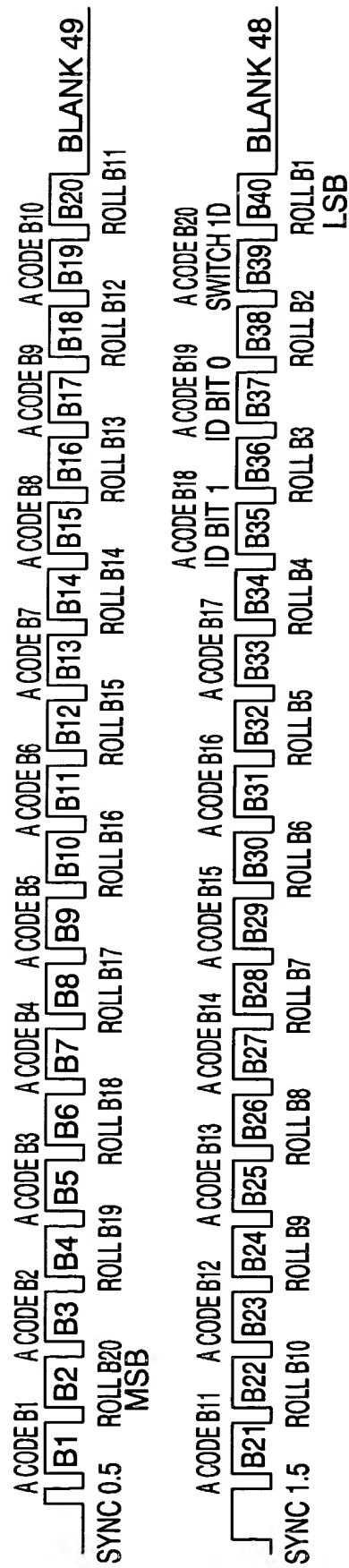
Fig. 4

Fig. 5A
Fig. 5B

Fig. 5A







ACTIVE TIME BIT VALUE



BIT TIMING

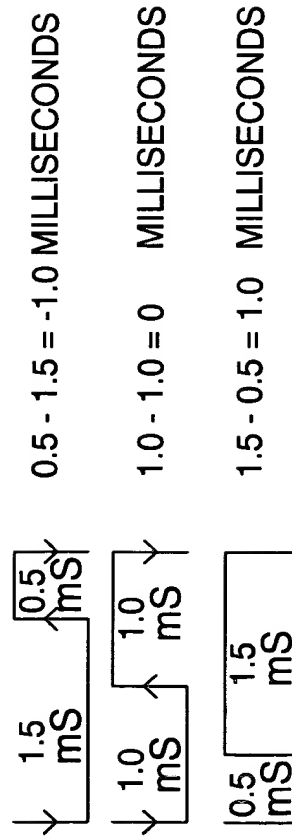


Fig. 6

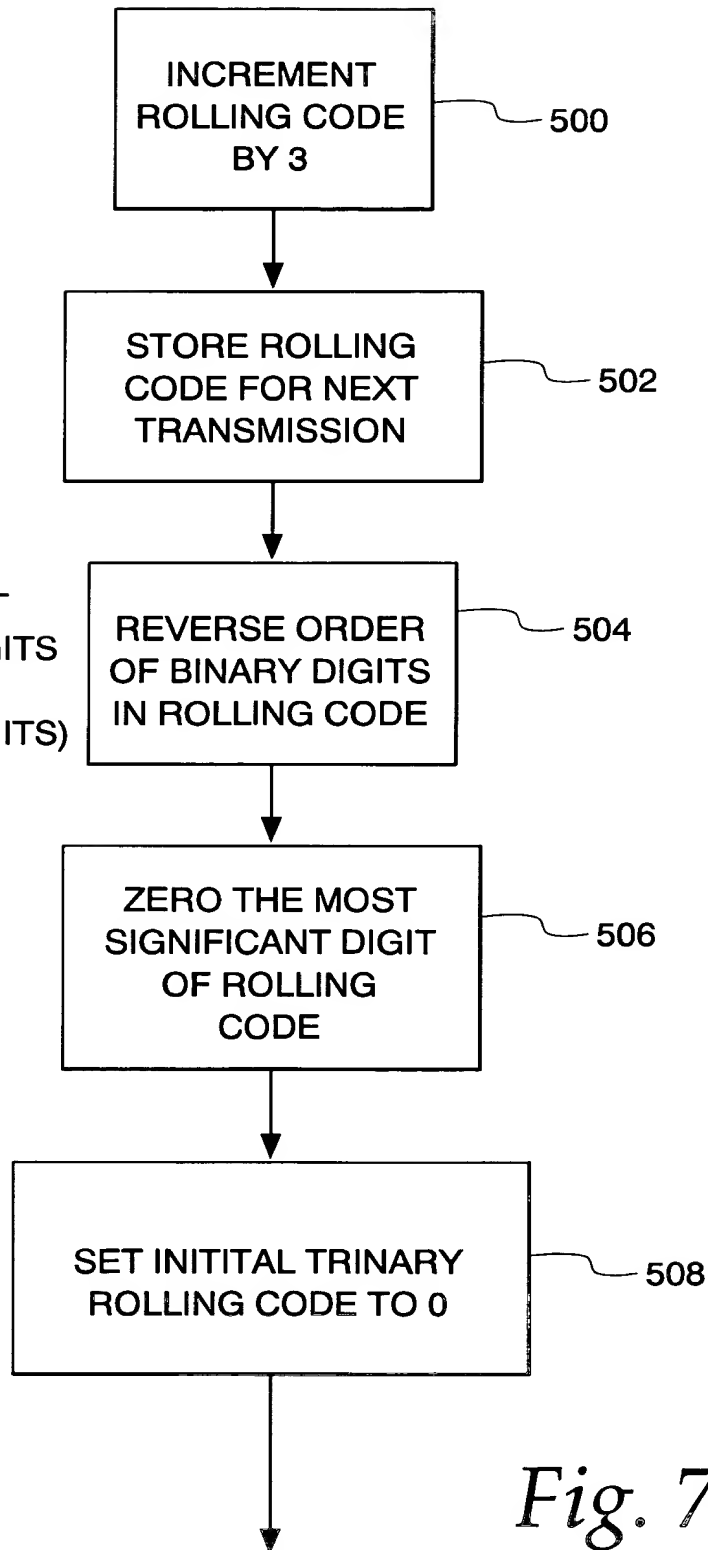
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*Fig. 7A*

*Fig. 7B*

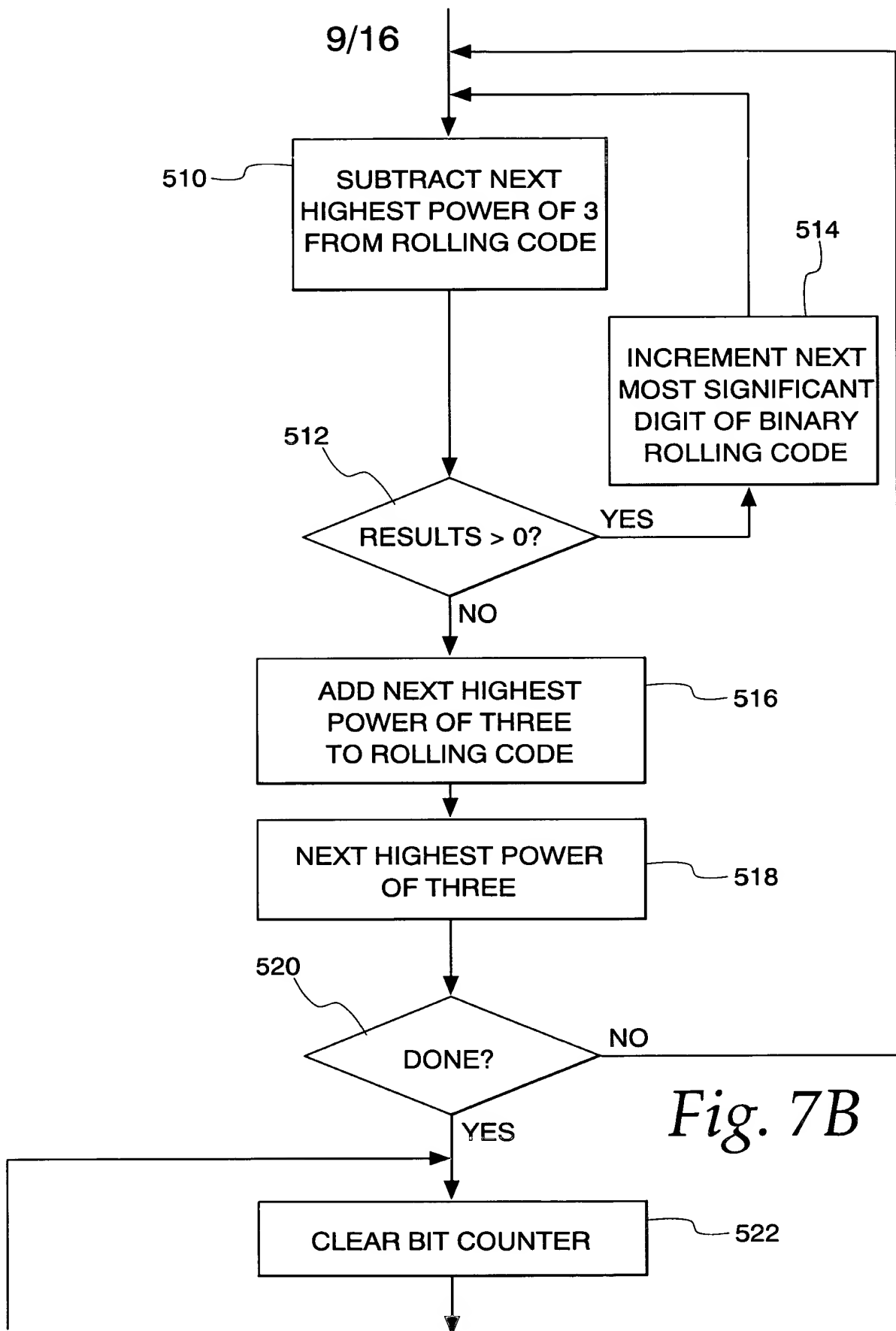
*Fig. 7C*

(REVERSE MOST  
SIGNIFICANT DIGITS  
WITH LEAST  
SIGNIFICANT DIGITS)

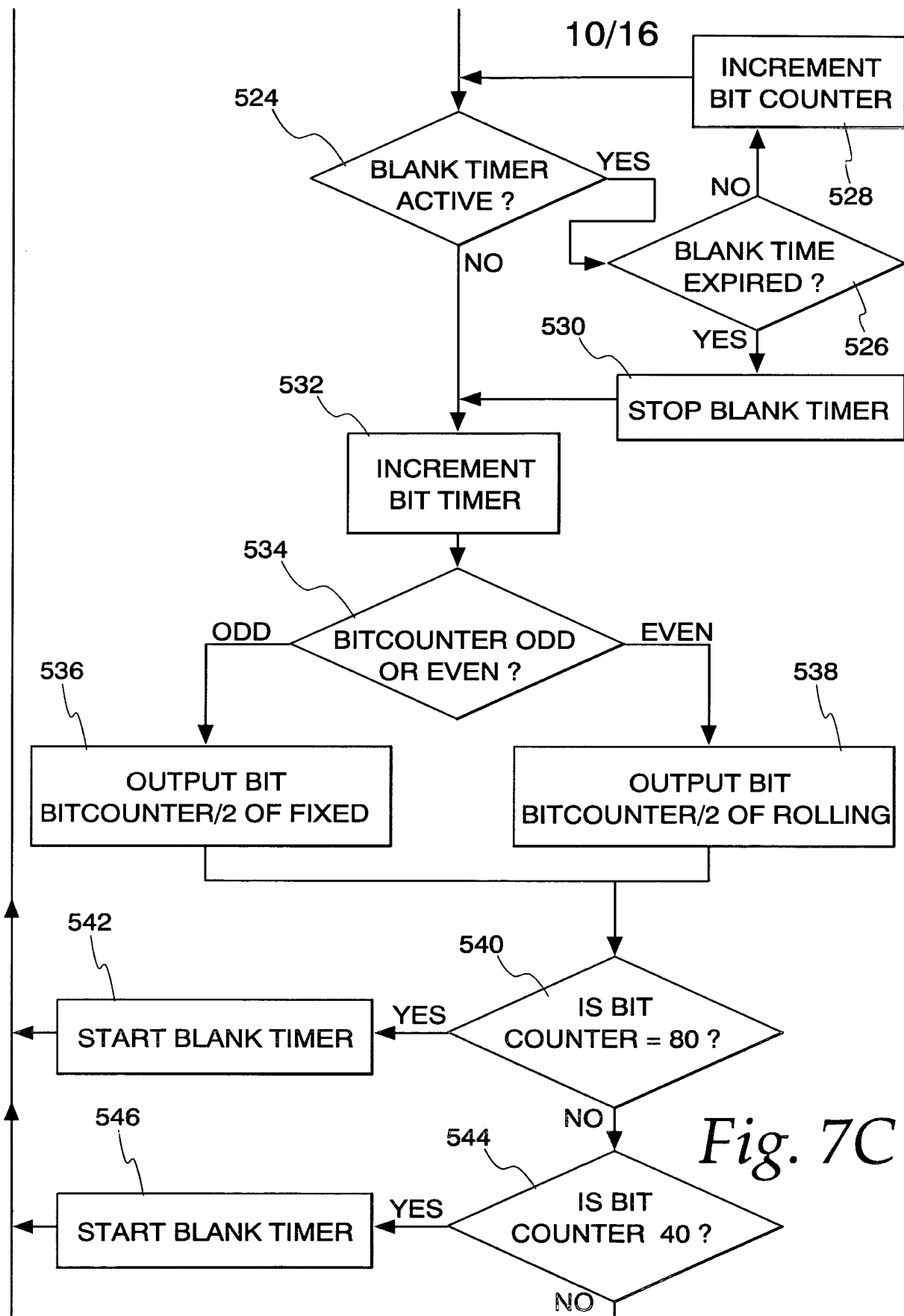


*Fig. 7A*

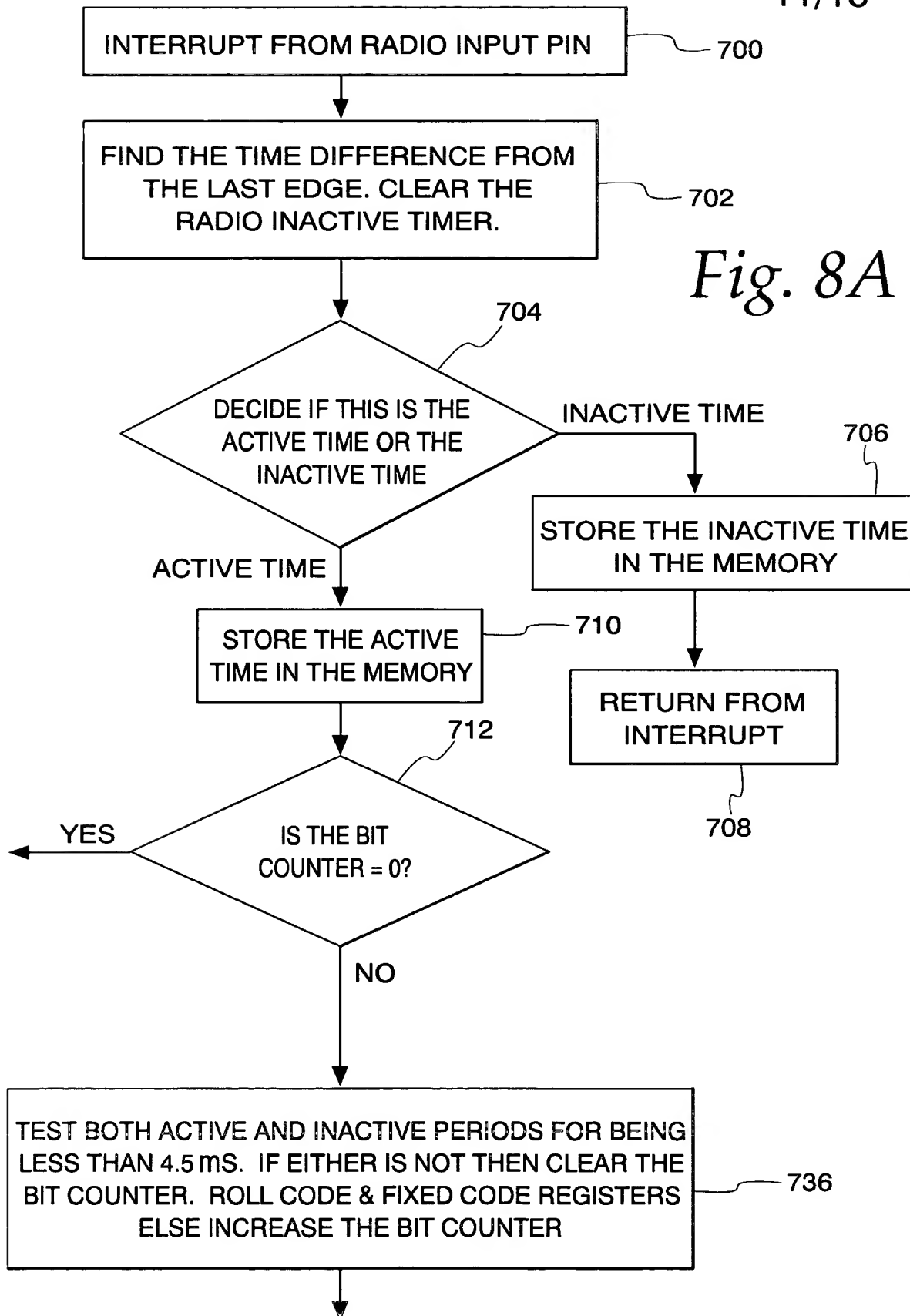




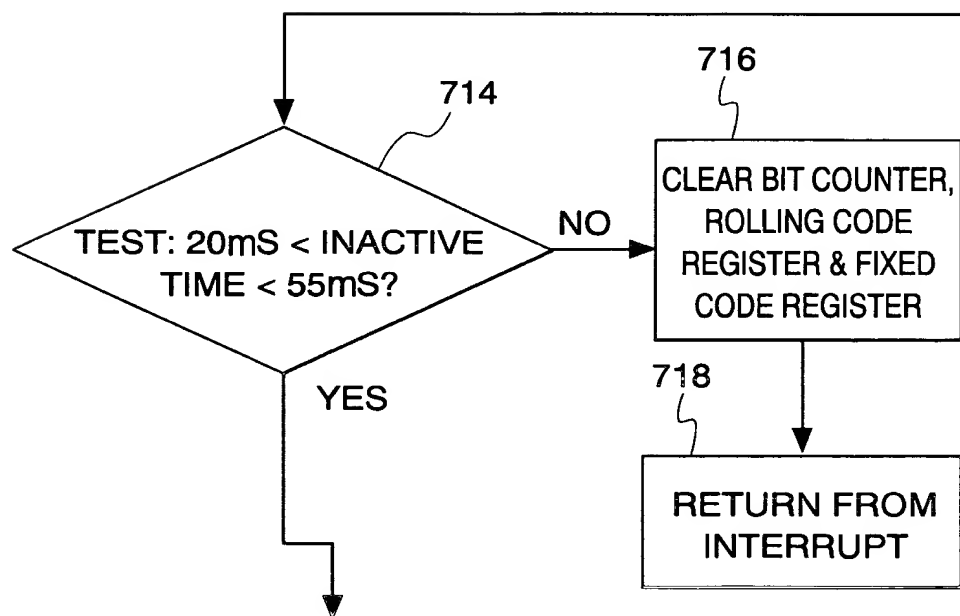
*Fig. 7B*

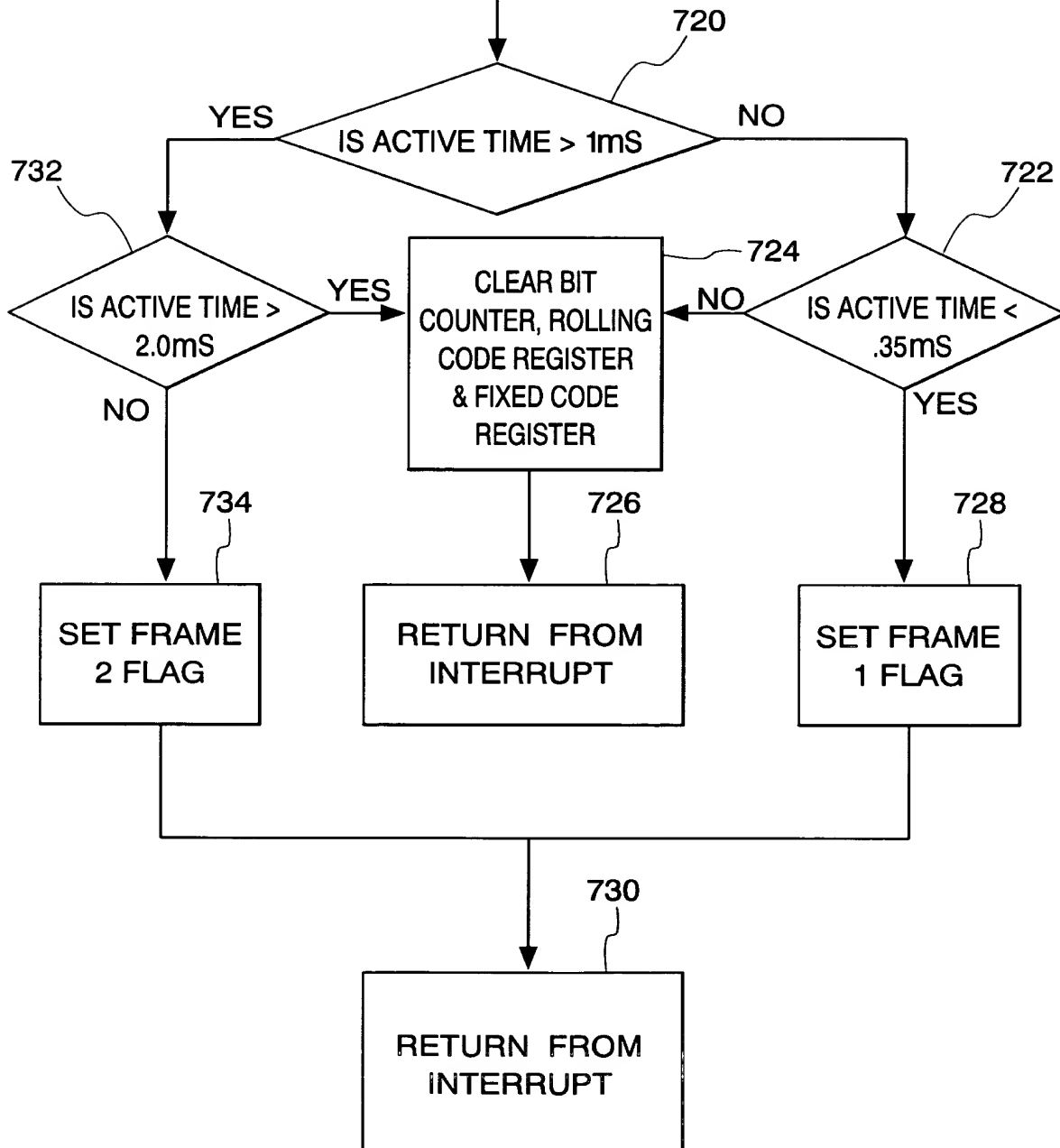


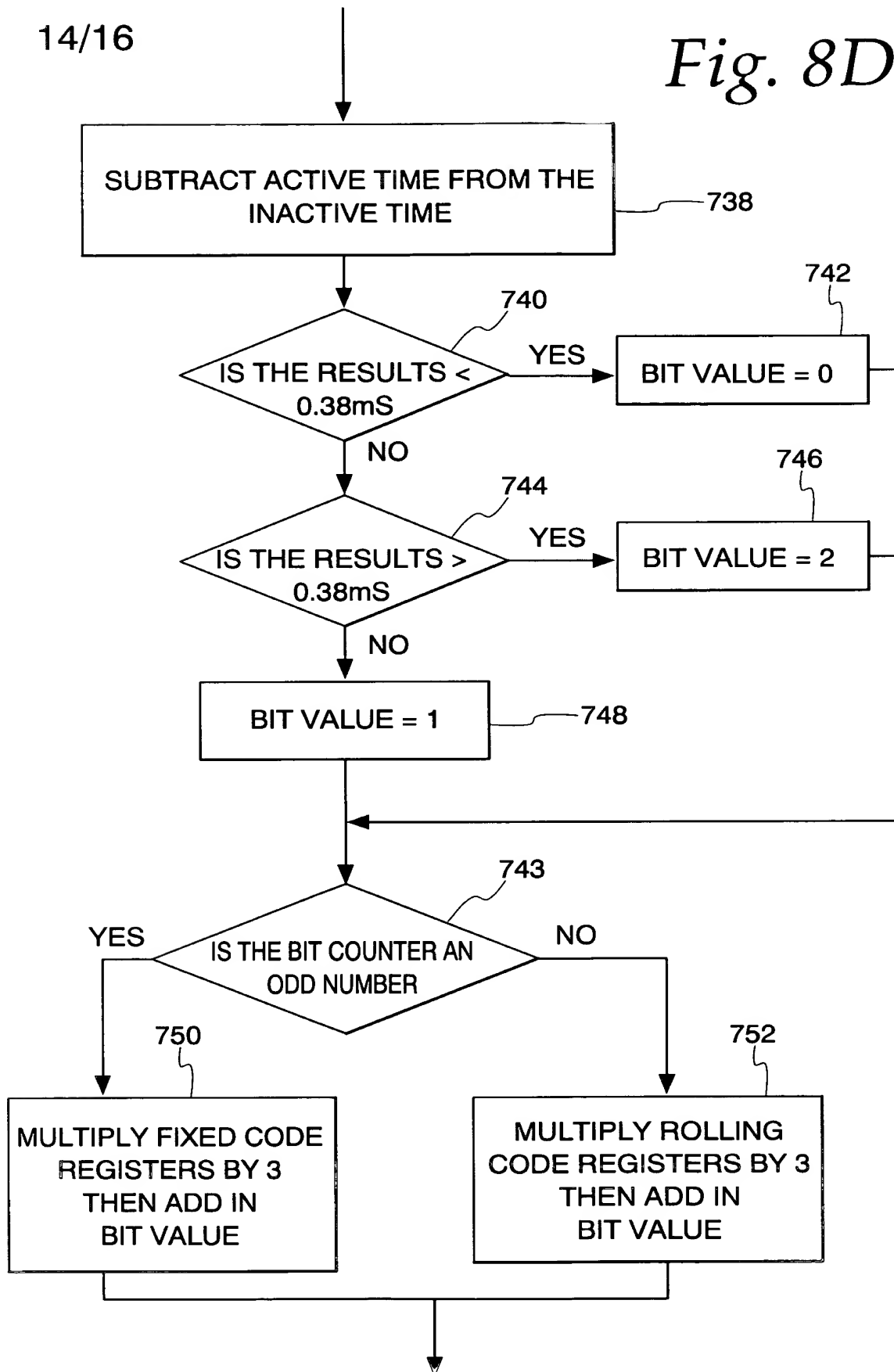
*Fig. 7C*

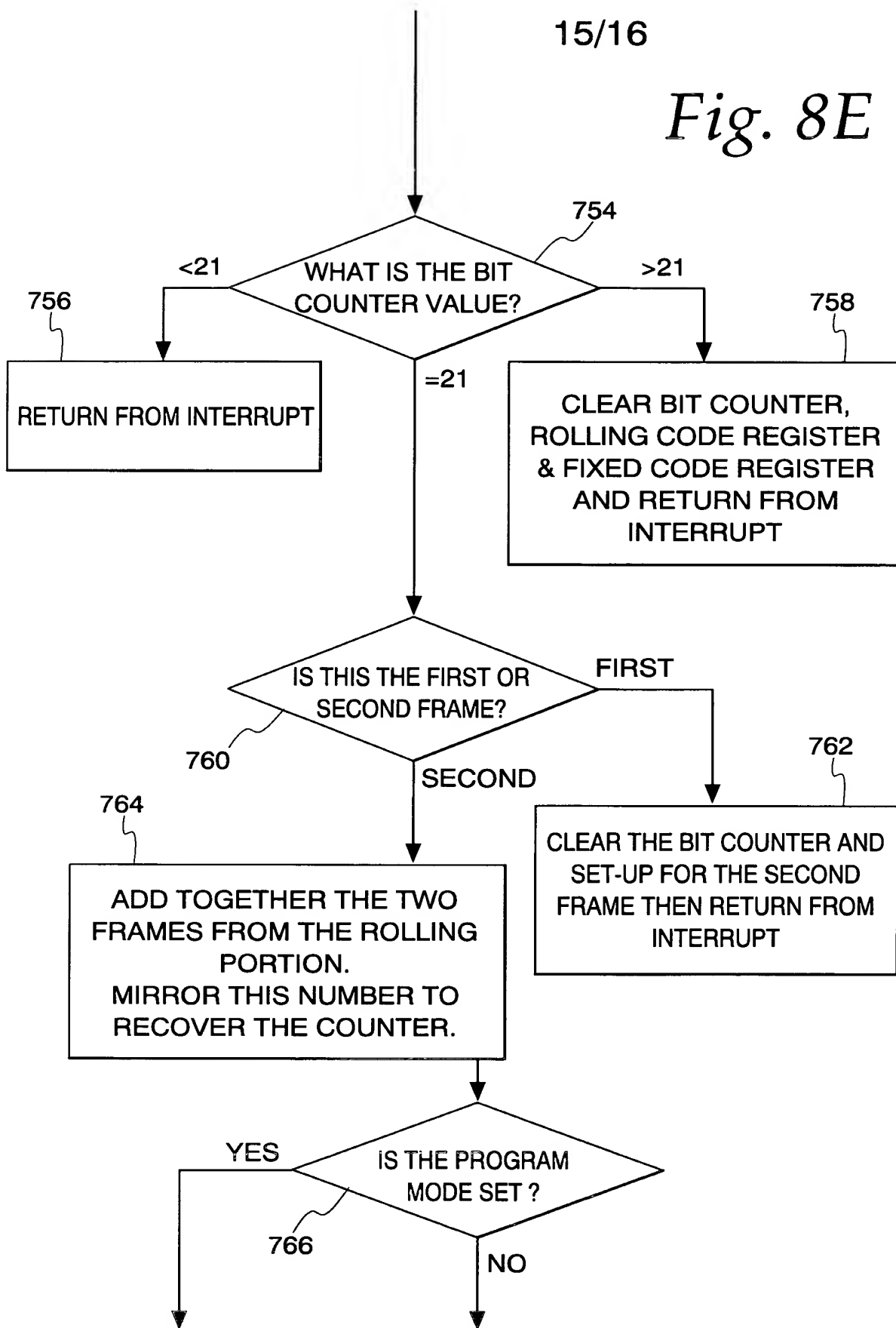


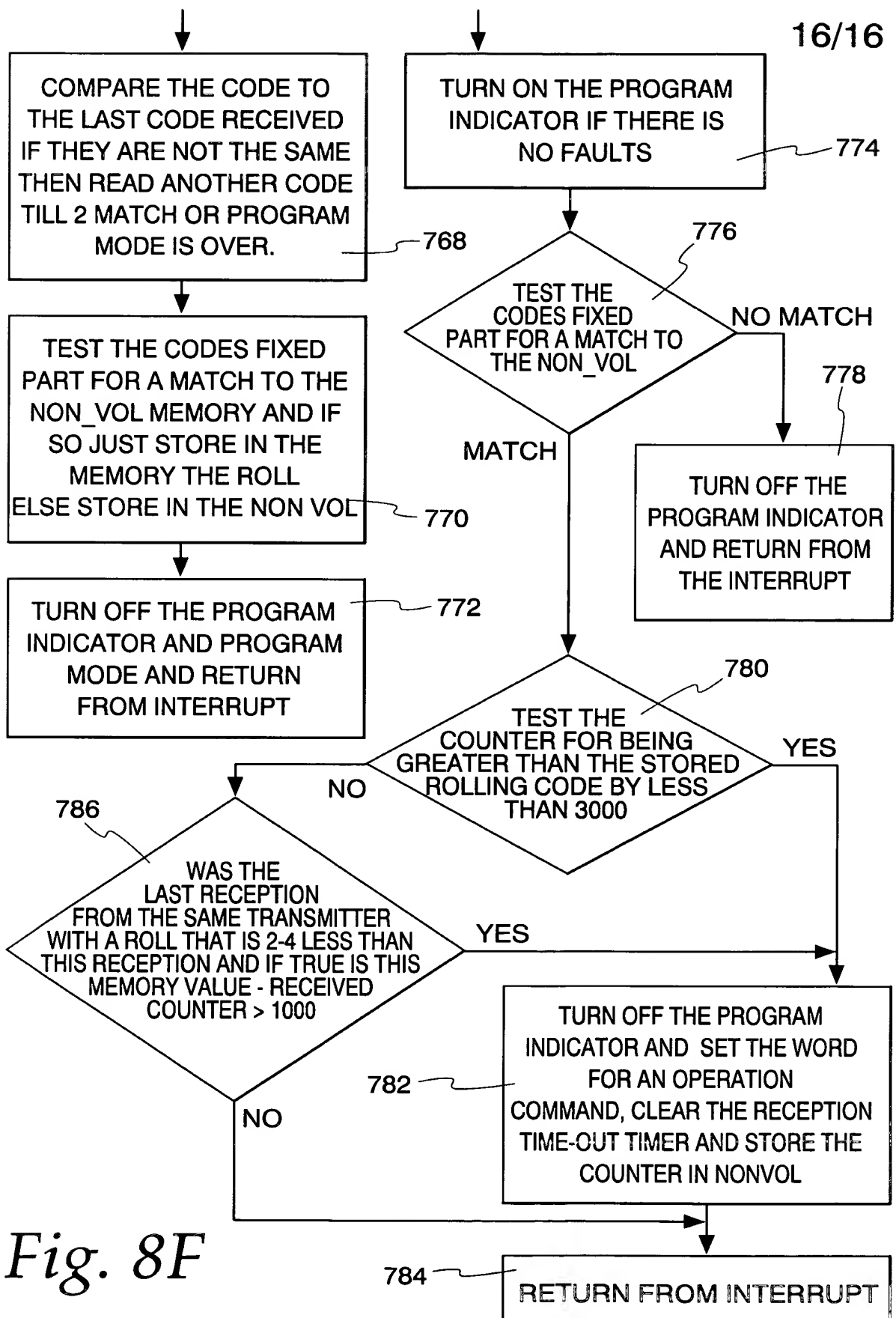
<i>Fig. 8B</i>	<i>Fig. 8A</i>
<i>Fig. 8C</i>	<i>Fig. 8D</i>
	<i>Fig. 8E</i>
	<i>Fig. 8F</i>

*Fig. 8B*

*Fig. 8C*



*Fig. 8E*



*Fig. 8F*